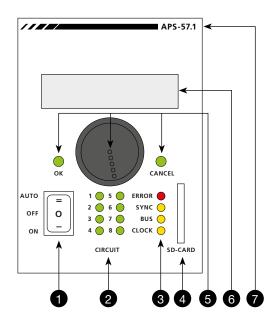
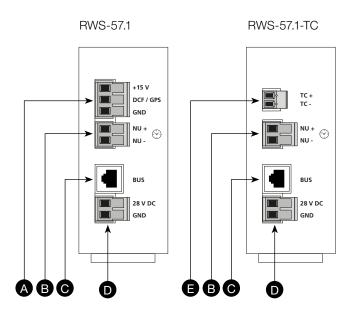


APS-57.1

Master clock with 8 circuits





Front view

- 1 Switch ON/OFF/AUTO
- 2 LED Indicator for circuits
- 3 LED Status display
- 4 SD Card slot
- 5 Controls for menu navigation
- 6 LCD Display for mode indication
- 7 Model code

Rear view

- A Connector block for DCF/GPS
- B Connector block for slave clocks
- C RJ11 bus for future expansions
- D Connector block for external power infeed
- E Connector block for synchronisation

Description

The time switch makes available eight time-controlled I 2 C circuits which are used as binary contacts in the APS-APROSYS system. The switching times can be configured conveniently with a PC program and are saved to the SD card. In addition, the module is able to control slave clocks up to 1 A with TC or pulse method.

 $\label{lem:constraints} \mbox{Featuring additional, programmable remote switch-on.}$

Configuration of the switching times

Via PC graphic user interface (GUI). Software GM-HU stored on SD card



Quartz 20 ppm/DCF < 1 ppm

64 kbps fix/44.1 kHz/mono

1 A (The output features electronic short-circuit protection

which switches back on again

Pulse memory with fast pulses

Max. 85 %, no condensation

for master clock operation

10 years (Lithium battery)

automatically)

Selectable

Selectable

Selectable

500

600 g

after power failure

For APS System

-5°C to 40°C

28-30 VDC/2 A

FAT16/max. 4 GB

Technical data

Connection diagram for connector block (A)

1) +15 V

2) DCF/GPS

3) Ground

1 2 3

General information

Accuracy

Soundfile

Pulse output

Slave clock mode - Minute pulse

- Time Code (TC)

Power reserve

Time memory

Signal points

Weight

SD Card

Digital outputs 1-8

Relative humidity

Connection voltage

Ambient temperature

- Time Code Pol (TCPOL)

Connection DCF: 2 and 3 Connection GPS: 1, 2 and 3

Connection diagram for connector block (B)

1) Output slave clock +

2) Output slave clock -



J11 bus for future expansions (C)

RJ11 Bus



Connection diagram for connector block (D)

1) 28 VDC

2) GND



Connection diagram for connector block (E)

1) Input slave clock +

2) Input slave clock -



DIP Switch

- 1) Switch ON/OFF/AUTO, active or not active
- 2) Addressing for future expansions
- 3) Addressing for future expansions
- 4) Addressing for future expansions



Switch ON/OFF/AUTO

AUTO Time-controlled master switch

OFF OFF, accumulator OFF (deep discharge protection)

ON ON

LED Status display

ERROR Error

SYNC DCF/GPS Synchronisation

SOUND Sound File played
CLOCK Impulse slave clock line

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